

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Masato Yoshioka et al.

Application. No.: TBD

Filed: December 10, 2003

Title: INTEGRATED CIRCUIT AND A/D CONVERSION CIRCUIT

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Group Art Unit: TBD

Examiner: TBD

PRELIMINARY AMENDMENT

Mail Stop PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Applicants submit the following amendments and remarks for entry and consideration prior to the examination of the above-identified application.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks begin on page 6 of this paper.